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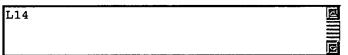
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<u>L11</u>	(creat\$ or generat\$ or develop\$ or mak\$) near4 (relocation\$ near4 instruction\$) (compil\$ Or assembl\$ or machine\$ or object\$) near8 (code\$ or instruction\$ or program\$)	0	<u>L11</u>
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<u>L9</u>	instruction\$) (compil\$ Or assembl\$ or machine\$ or object\$) near8 (code\$ or instruction\$ or program\$)	0	<u>L9</u> -	
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<u>L8</u>	(creat\$ or generat\$ or develop\$ or mak\$) near4 (relocation\$ near4 instruction\$) (compil\$ Or assembl\$ or machine\$ or object\$) near8 (code\$ or instruction\$ or program\$)	0	<u>L8</u>	
DB=PGPB; PLUR=YES; OP=ADJ				
<u>L7</u>	(creat\$ or generat\$ or develop\$ or mak\$) near4 (relocation\$ near4 instruction\$) (compil\$ Or assembl\$ or machine\$ or object\$) near8 (code\$ or instruction\$ or program\$)	1	<u>L7</u>	
DB=	USPT; PLUR=YES; OP=ADJ			
<u>L6</u>	(replac\$ near9 (relocation\$ near5 instruction\$))	2	<u>L6</u>	
<u>L5</u>	(replac\$ near9 relocation\$)	189	<u>L5</u>	
<u>L4</u>	L3 and (replac\$ near9 relocation\$)	0	<u>L4</u>	
<u>L3</u>	L2 and (compil\$ Or assembl\$ or machine\$ or object\$) near8 (code\$ or instruction\$ or program\$)	13	<u>L3</u>	
<u>L2</u>	(creat\$ or generat\$ or develop\$ or mak\$) near4 (relocation\$ near4 instruction\$)	13	<u>L2</u>	
<u>L1</u>	(creat\$ or generat\$) near4 (relocation\$ near4 instruction\$) and (object or machine\$) near4 (code\$ or program\$)	6	<u>L1</u>	

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Richard W. Conway, Thomas R. Wilcox

March 1973 Communications of the ACM, Volume 16 Issue 3

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PL/C is a compiler for a dialect for PL/I. The design objective was to provide a maximum degree of diagnostic assistance in a batch processing environment. For the most part this assistance is implicit and is provided automatically by the compiler. The most remarkable characteristic of PL/C is its perseverance—it completes translation of every program submitted and continues execution until a user-established error limit is reached. This requires that the compiler repair errors encoun ...

Keywords: PL/I, compilers, debugging, programming languages

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Anita K. Jones, Peter Schwarz

June 1980 ACM Computing Surveys (CSUR), Volume 12 Issue 2

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²³ Prefetching using Markov predictors

Doug Joseph, Dirk Grunwald

May 1997 ACM SIGARCH Computer Architecture News , Proceedings of the 24th annual international symposium on Computer architecture, Volume 25 Issue 2

Full text available: pdf(1.68 MB)

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Prefetching is one approach to reducing the latency of memory operations in modern computer systems. In this paper, we describe the Markov prefetcher. This prefetcher acts as an interface between the on-chip and off-chip cache, and can be added to existing computer designs. The Markov prefetcher is distinguished by prefetching multiple reference predictions from the memory subsystem, and then prioritizing the delivery of those references to the processor. This design results in a pr ...

Migrating a CISC computer family onto RISC via object code translation

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